

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER OF PATENTS AND TRADEMARKS Washington, D.C. 20231 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/002,868	11/15/2001	Michael Belman	P05378US0	4795
22885 7.	590 05/16/2002			
MCKEE, VOORHEES & SEASE, P.L.C. 801 GRAND AVENUE SUITE 3200			EXAMINER	
			SEFER, AHMED N	
DES MOINES, IA 50309-2721			ART UNIT	PAPER NUMBER
			2826	
		DATE MAILED: 05/16/2002		

Please find below and/or attached an Office communication concerning this application or proceeding.

PTO-90C (Rev. 07-01)

	_	p-				
	Application No.	Applicant(s)				
,,	10/002,868	BELMAN, MICHAEL				
Office Action Summary	Examiner	Art Unit				
	A. Sefer	2826				
The MAILING DATE of this communication apperiod for Reply						
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a rep - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statute - Any reply received by the Office later than three months after the mailin earned patent term adjustment. See 37 CFR 1.704(b). Status	136(a). In no event, however, may a ly within the statutory minimum of the will apply and will expire SIX (6) MC as cause the application to become a	a reply be timely filed irty (30) days will be considered timely. DNTHS from the mailing date of this communication. ABANDONED (35 U.S.C. § 133).				
1) Responsive to communication(s) filed on						
 /-	his action is non-final.					
3) Since this application is in condition for allow	rance except for formal m	atters, prosecution as to the merits is				
closed in accordance with the practice under Disposition of Claims	Ex parte Quayle, 1905 C					
4) Claim(s) 1-8 is/are pending in the application		•				
4a) Of the above claim(s) is/are withdra	awn from consideration.					
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>1-8</u> is/are rejected.						
7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and/	or election requirement.					
Application Papers		•				
9) The specification is objected to by the Examiner.						
10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a). 11) ☐ The proposed drawing correction filed on is: a) ☐ approved b) ☐ disapproved by the Examiner.						
If approved, corrected drawings are required in reply to this Office action.						
12) The oath or declaration is objected to by the Examiner.						
Priority under 35 U.S.C. §§ 119 and 120						
	an priority under 35 U.S.C	c. § 119(a)-(d) or (f).				
13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of:						
1. Certified copies of the priority documents have been received.						
2. Certified copies of the priority documents have been received in Application No						
3. Copies of the certified copies of the priority documents have been received in this National Stage						
application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.						
14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).						
a) The translation of the foreign language p	rovisional application has	been received.				
Attachment(s)	, ,					
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s)	5) Notice	ew Summary (PTO-413) Paper No(s) of Informal Patent Application (PTO-152)				

Application/Control Number: 10/002,868

Art Unit: 2826

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 1-3 and 5-8 are rejected under 35 U.S.C. 102(b) as being anticipated by Chapel, Jr. et al. US Patent No. 4,901,052.

Chapel, Jr. et al disclose (see fig. 5, col. 1, lines 20-25 and col. 9, lines 7-12) a chip resistor comprising a substrate 74 having opposite parallel symmetrical first and second surfaces, and a central longitudinal plane of symmetry; separate and spaced first and second resistive layers 76 and 78, or thick film resistive layers (as in claim 2) or thin film resistive layers (as in claim 3) on the first and second surfaces, respectively, electrically connected in parallel to each other or connected in parallel by end terminals on ends of a substrate (as in claim 6), wherein the terminals are adopted for mounting to a circuit board (as in claim 7); and the first and the second surfaces of the substrate being symmetrically located with respect to and equidistant from the central longitudinal plane so that when electrical current passes through the resistive layers, a temperature distribution within the substrate will be substantially symmetrical about the central longitudinal plane of the substrate for eliminating thermal bending thereof.

Application/Control Number: 10/002,868

Art Unit: 2826

As to claim 5, Chapel, Jr. et al disclose an area of the first resistive layer is substantially equal to that of the second resistive layer such that the chip resistor with both resistive layers tolerates higher instantaneous pulsed power than either layer could provide separately and individually without the other resistive layer.

As to claim 8, Chapel, Jr. et al disclose first resistive layer and second resistive layer are symmetric about the central longitudinal plane.

Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 4. Claim 4 is rejected under 35 U.S.C. 103(a) as being unpatentable over Chapel, Jr. et al. US Patent No. 4,901,052 in view of Thompson US Patent No. 4,064,477.

Chapel, Jr. et al discloses all the claimed subject matter but does not specifically teach the use of foil resistive layers.

Thompson discloses (see col. 1, lines 5-26) a foil resistive layer.

Therefore, it would have been obvious to one skilled in the art at the time the invention was made to substitute the thin or thick film resistive layer of the prior art with a foil resistive layer, since that would allow a uniform current density.

Application/Control Number: 10/002,868

Art Unit: 2826

Conclusion

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Schneider et al. (DE 3035717) disclose the advantage of using a foil resistive layer.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to A. Sefer whose telephone number is (703) 605-1227.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nathan J Flynn can be reached on (703) 308-6601.

ANS May 10, 2002

> NATHAN .J. FLYNN SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2800